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PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date May 25, 2007 Denise Sheridan
Denise Sheridan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Beth E. Skidmore Attorney Docket No.: 501256.01
Patent No. : 7,082,075 B2 Serial No. : 10/804,249
Issue Date : July 25, 2006 Filed : March 18, 2004
Title : MEMORY DEVICE AND METHOD HAVING BANKS OF DIFFERENT SIZES

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
JUN 01 2007
of Correction

Sir:

A Certificate of Correction under 35 U.S.C. § 254 is respectfully requested for the above-identified patent in order to correct errors made during the printing of the patent. The changes in the patent needed to correct the errors are as follows:

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
First page, Column 2	Cited references 6,597,621, 6,603,705, 6,754,117, 6,754,118, 6,760,274 and 6,845,432 all read B1	Cited references 6,597,621, 6,603,705, 6,754,117, 6,754,118, 6,760,274 and 6,845,432 should read B2
Second page, Column 1	Cited references 6,862,229, 6,906,960, 6,928,023 and 6,965,980 all read B1	Cited references 6,862,229, 6,906,960, 6,928,023 and 6,965,980 should read B2

05/30/2007 HDESTA1 00000014 501266 7082075
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Drawing Sheet 1,
Figure 1: Reference
number 10 should be
added to reflect the
entire memory
device.

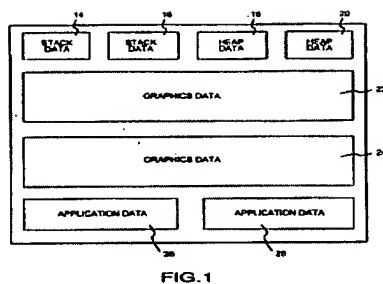


FIG. 1

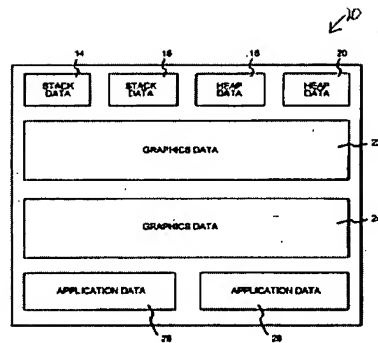


FIG. 1

Drawing Sheet 2,
Figure 2: Reference
number 128d should
reference the sense
amplifier box
entitled, "SA-3".

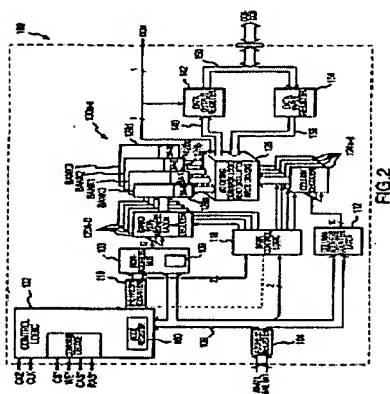


FIG. 2

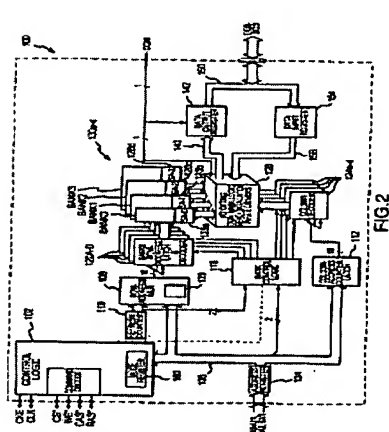


FIG. 2

Column 1, Line 31 "sense amplifiers provided"
Column 2, Lines 46-47 "columns of memory cells of memory cells."
Column 2, Line 59 "banks 14, 16 18 and 20"
Column 3, Lines 15-16 "in FIG. 1 is a"
Column 3, Line 42 "array 130a-d"
Column 3, Line 46 "arrays 130a-d"
Column 4, Line 57 "in the arrays 120a-d"
Column 5, Line 64 "memory controller 230"
Column 6, Lines 5-6 "memory controller 230"

--sense amplifier provided--
--columns of memory cells.--
--banks 14, 16, 18 and 20--
--in FIG. 2 is a--
--memory bank 130a-d--
--memory banks 130a-d--
--in the banks 130a-d--
--system controller 214--
--system controller 214--

Column 7, Lines 32-33	"banks at a location determined"	--banks at a location determined--
Column 8, Line 65	"command signals;"	--command signals; and--

Some of the above errors for which correction is requested under 35 U.S.C. § 254 were made in the printing of the patent. In addition, some errors were inadvertently made in the original application. The errors are considered sufficiently important to justify the processing of a Certificate of Correction under 35 U.S.C. § 254. A Form PTO-1050, in duplicate, is enclosed herewith.

Since some of the errors made by the Applicant are typographical in nature and inadvertently made in the original application and would normally be shown on a Notification of Errors, The Commissioner is hereby authorized to charge the \$100 fee associated with this communication to Deposit Account No. 50-1266. A duplicate copy of this sheet is enclosed.

Favorable consideration of this Request is respectfully requested.

Respectfully submitted,

Date: May 23, 2007

By: Edward W. Bulchis
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Customer No. 27,076
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EWB:dms

Enclosures:

Postcard

Form PTO-1050 (+ copy)

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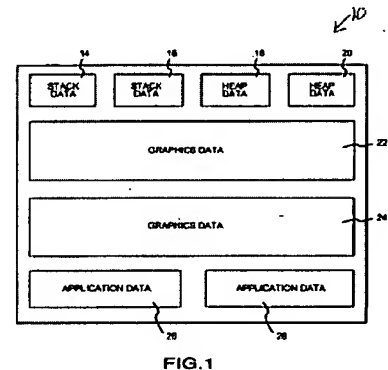
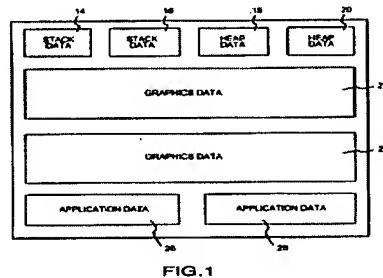
UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,082,075 B2
DATED : July 25, 2006
INVENTOR(S) : Beth E. Skidmore

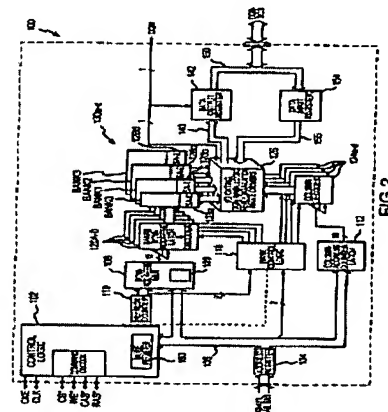
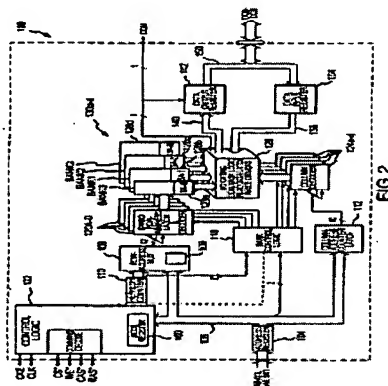
It is certified that error appears in the above identified patent and that said Letters Patent is hereby corrected as shown below:

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Column 1, Line 31

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FORM PTO-1050 (REV. 3-82)

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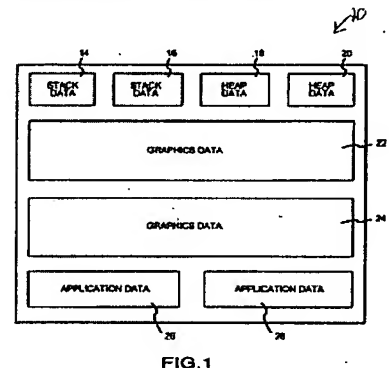
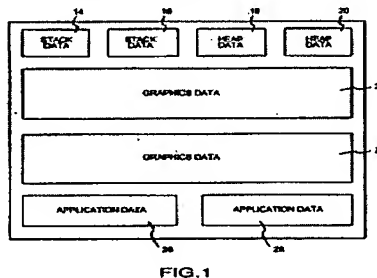
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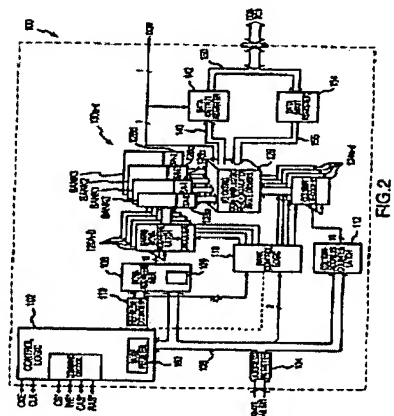
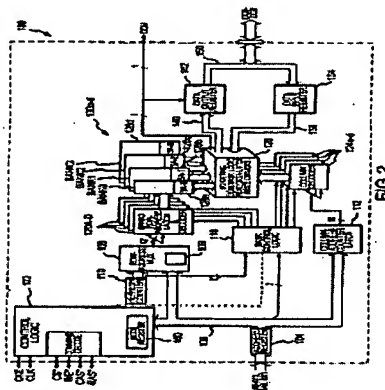
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